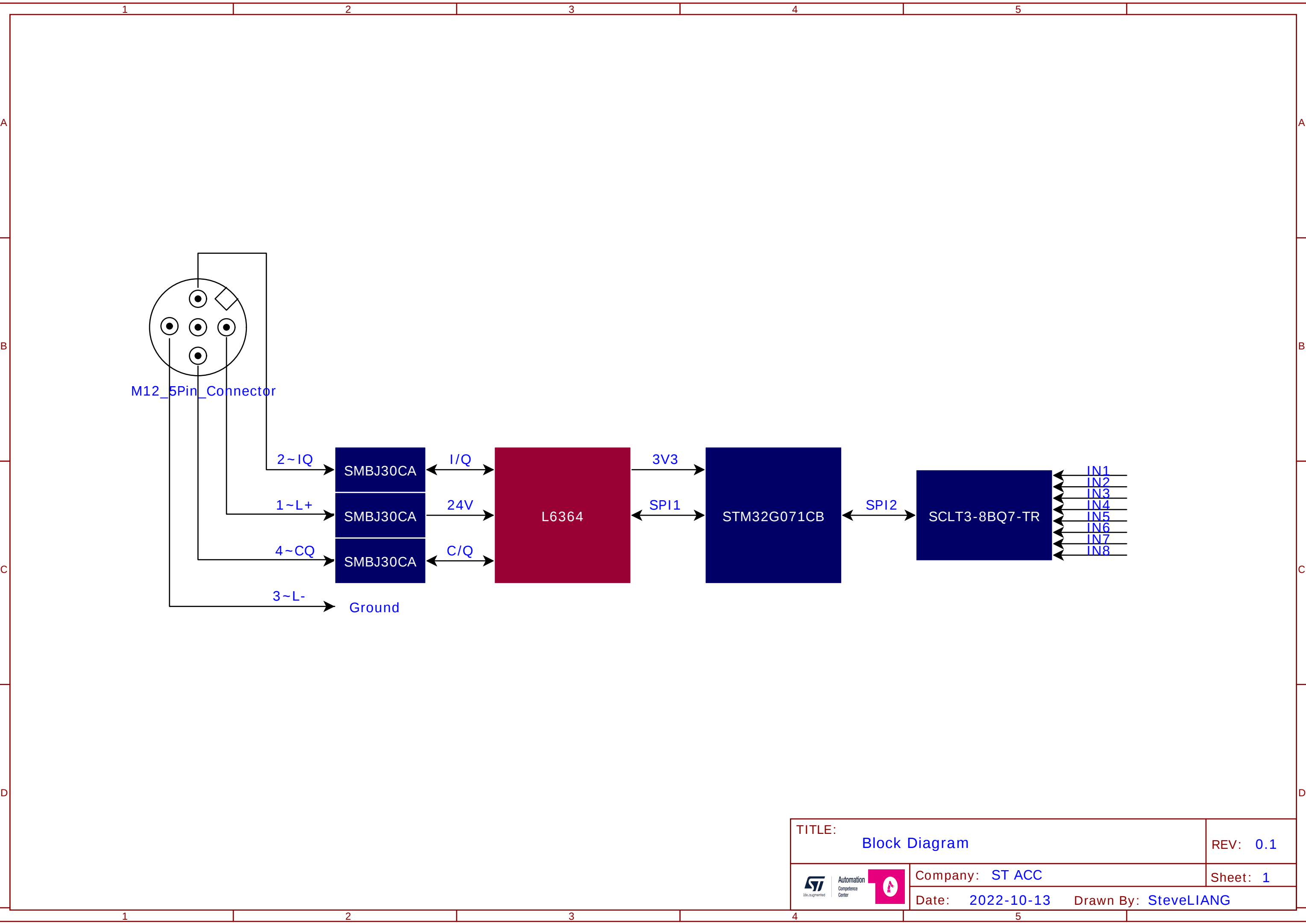
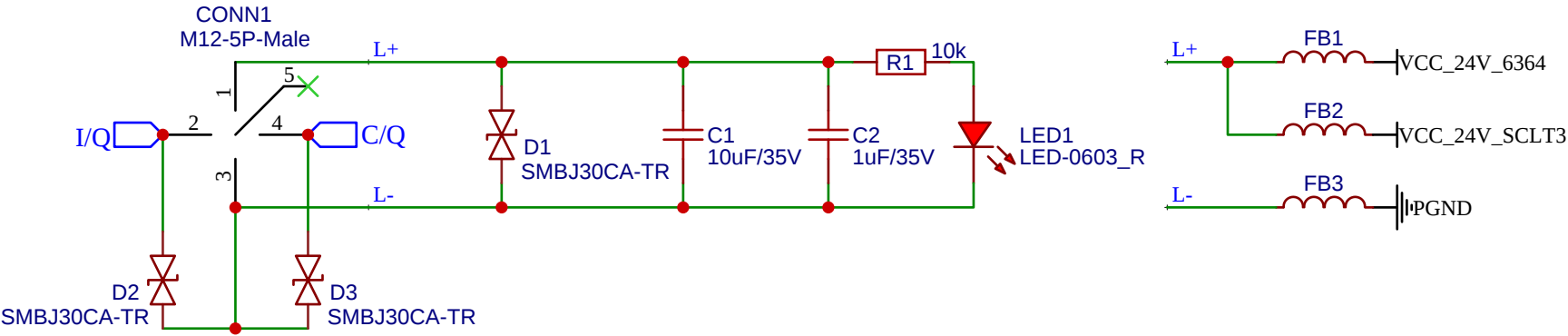
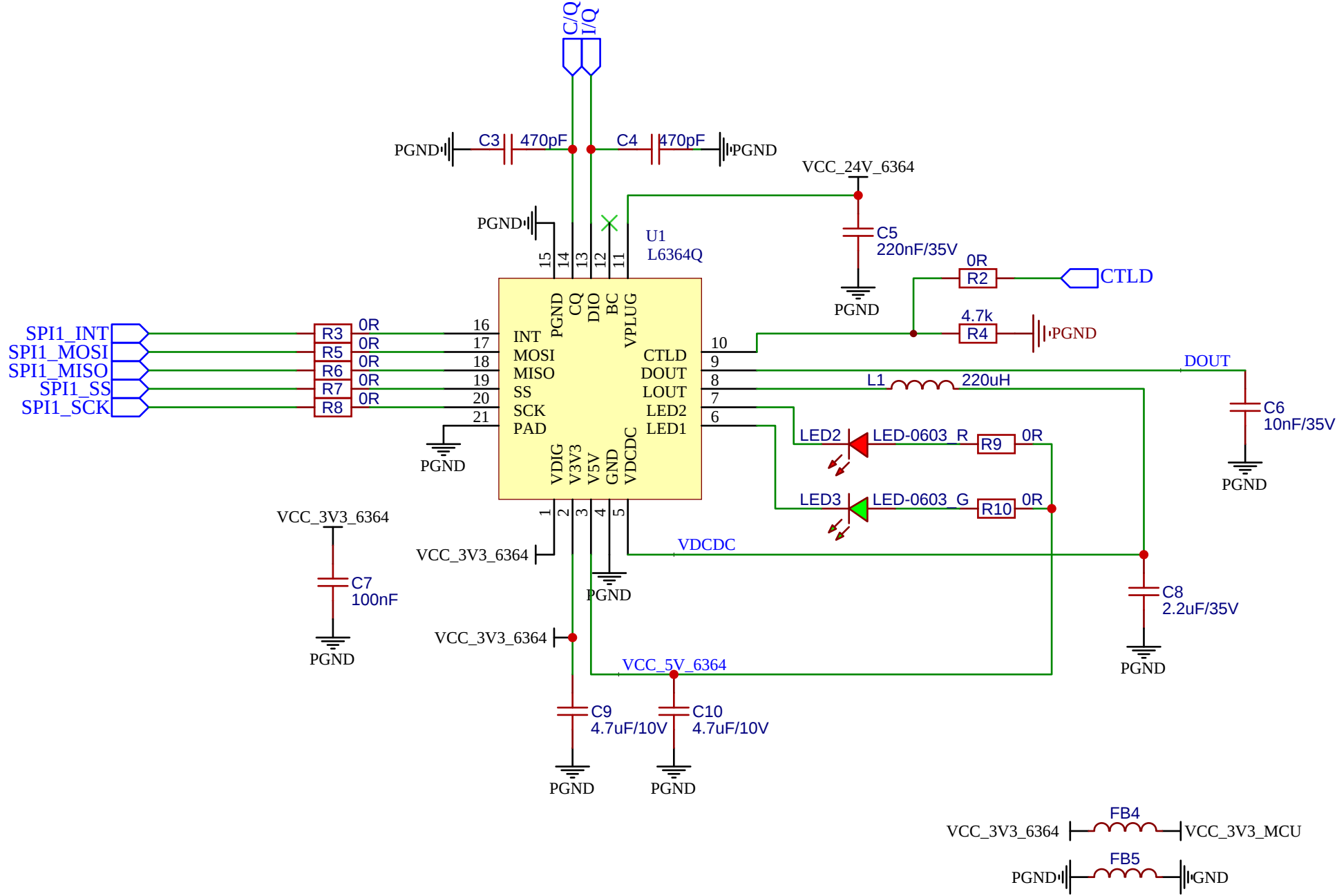




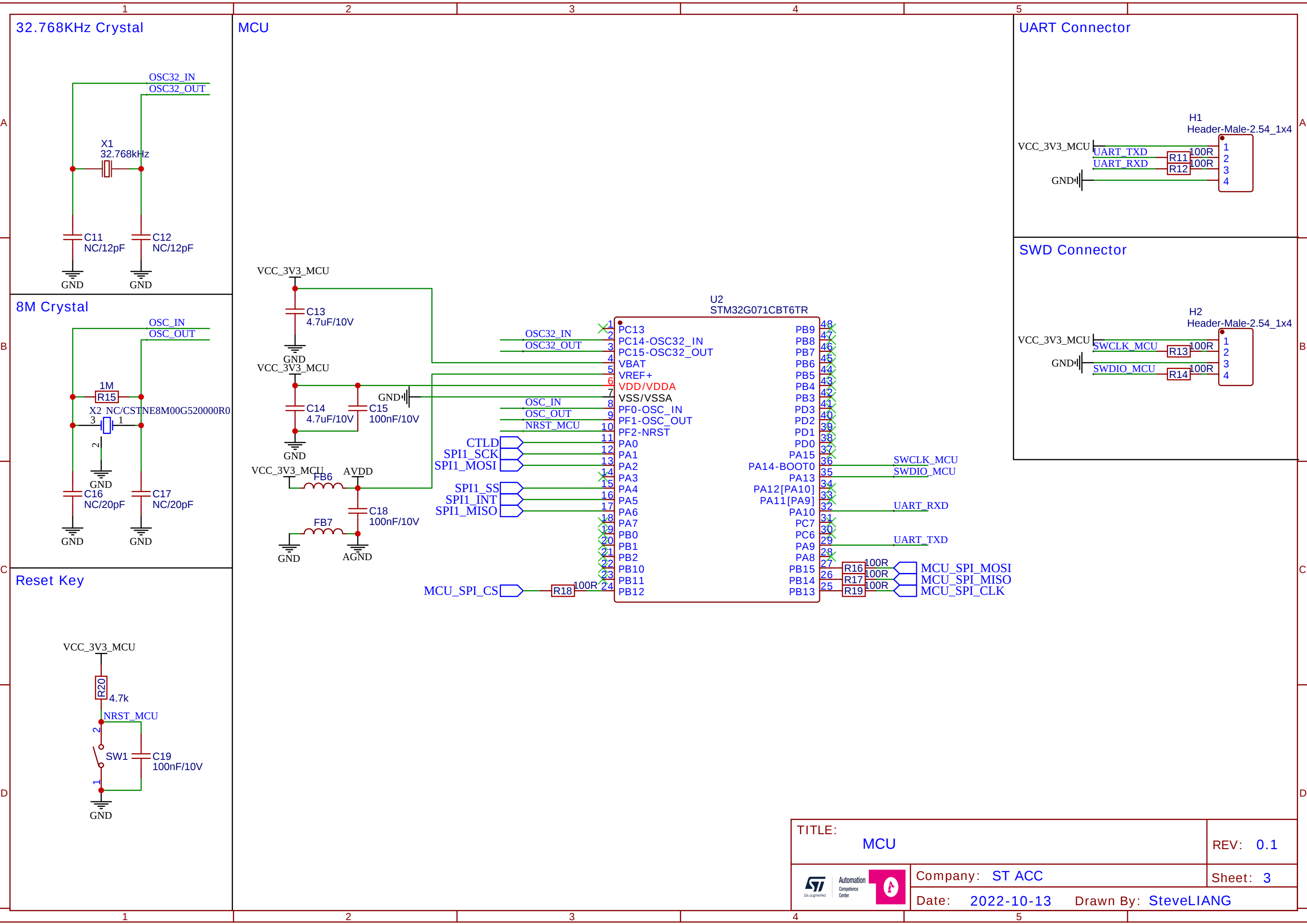
M12 5Pin Connector



L6364



TITLE: Power Management & L6364		REV: 0.1
Company: ST ACC		Sheet: 2
Date: 2022-10-13		Drawn By: SteveLIANG



The schematic diagram illustrates the SCLT3-8BQ7-TR module and its associated components. The module is a central component with multiple pins and internal components. The connections are as follows:

- Power Supply:** VCC_24V_SCLT3 is connected to the module's VCC pin (pin 1) and the VCC pin of the voltage converter (U3, U4, U6, U7). A 1.5M resistor (R21) and a 120k resistor (R24) are connected between VCC_24V_SCLT3 and GND. A 100nF/35V capacitor (C23) is connected between VCC_24V_SCLT3 and GND.
- Ground:** GND is connected to the module's GND pin (pin 2) and the GND pin of the voltage converter (U3, U4, U6, U7).
- Signal Lines:**
 - SCLT3_SPI_CS: Connected to the module's CS# pin (pin 48) and the CS# pin of the voltage converter (U3, U4, U6, U7).
 - SCLT3_SPI_CLK: Connected to the module's SCK pin (pin 47) and the CLK pin of the voltage converter (U3, U4, U6, U7).
 - SCLT3_SPI_MOSI: Connected to the module's MOSI pin (pin 46) and the MOSI pin of the voltage converter (U3, U4, U6, U7).
 - SCLT3_SPI_MISO: Connected to the module's MISO# pin (pin 45) and the MISO pin of the voltage converter (U3, U4, U6, U7).
- Internal Components:**
 - Resistors:** R22 (NM), R23 (OR), R25 (NM), R27 (OR), R29 (15k), R30 (470R), R31 (470R), R32 (470R), R33 (470R), R34 (470R), R35 (470R), R36 (470R), R37 (470R).
 - Capacitors:** C21 (4.7uF/10V), C22 (100nF/10V), C25 (10uF/35V), C26 (100nF/35V), C27 (1nF/35V), C28 (100nF/10V), C29 (100nF/10V).
 - Voltage Converter:** U3, U4, U6, U7 (SN74LV1T34DCKR).

Voltage Converter

The schematic diagram illustrates a Voltage Converter circuit. It features four SN74LV1T34DCKR buffers (U3, U4, U6, U7) connected to MCU SPI signals. The buffers are configured as follows:

- U3:** MCU_SPI_CS is connected to pin 1 (NC). Pin 2 (A) is connected to GND. Pin 3 (GND) is connected to GND. Pin 4 (Y) is connected to SCLT3_SPI_CS. Pin 5 (VCC) is connected to VDD.
- U4:** MCU_SPI_CLK is connected to pin 1 (NC). Pin 2 (A) is connected to GND. Pin 3 (GND) is connected to GND. Pin 4 (Y) is connected to SCLT3_SPI_CLK. Pin 5 (VCC) is connected to VDD.
- U6:** MCU_SPI_MOSI is connected to pin 1 (NC). Pin 2 (A) is connected to GND. Pin 3 (GND) is connected to GND. Pin 4 (Y) is connected to SCLT3_SPI_MOSI. Pin 5 (VCC) is connected to VDD.
- U7:** SCLT3_SPI_MISO is connected to pin 1 (NC). Pin 2 (A) is connected to GND. Pin 3 (GND) is connected to GND. Pin 4 (Y) is connected to VCC_3V3_MCU. Pin 5 (VCC) is connected to VDD.

The VDD supply is connected to a 100nF/10V capacitor (C20, C24, C28) and GND. The GND supply is connected to GND.

Inputs

VCC_24V_SCLT3 | D4 | STPS140Z | 1 | 2 | 3 | 4 | 5 | 3 | GND

CONN2
M12-5P-Female

R38 2.2k IN1 C30 3.3nF

VCC_24V_SCLT3 | D6 | STPS140Z | 1 | 2 | 3 | 4 | 5 | 3 | GND

CONN4
M12-5P-Female

R40 2.2k IN2 C32 3.3nF

VCC_24V_SCLT3 | D8 | STPS140Z | 1 | 2 | 3 | 4 | 5 | 3 | GND

CONN6
M12-5P-Female

R42 2.2k IN3 C34 3.3nF

VCC_24V_SCLT3 | D10 | STPS140Z | 1 | 2 | 3 | 4 | 5 | 3 | GND

CONN8
M12-5P-Female

R44 2.2k IN4 C36 3.3nF

VCC_24V_SCLT3 | D5 | STPS140Z | 1 | 2 | 3 | 4 | 5 | 3 | GND

CONN3
M12-5P-Female

R39 2.2k IN5 C31 3.3nF

VCC_24V_SCLT3 | D7 | STPS140Z | 1 | 2 | 3 | 4 | 5 | 3 | GND

CONN5
M12-5P-Female

R41 2.2k IN6 C33 3.3nF

VCC_24V_SCLT3 | D9 | STPS140Z | 1 | 2 | 3 | 4 | 5 | 3 | GND

CONN7
M12-5P-Female

R43 2.2k IN7 C35 3.3nF

VCC_24V_SCLT3 | D11 | STPS140Z | 1 | 2 | 3 | 4 | 5 | 3 | GND

CONN9
M12-5P-Female

R45 2.2k IN8 C37 3.3nF

				TITLE: SCLT3-8BQ7-TR		REV: 0.1
						Company: ST ACC
				Date: 2022-10-13		Sheet: 4 Drawn By: SteveLIANG
1	2	3	4	5		